

Company portrait: SECOR Chips & Library GmbH

SECOR Chips & Library GmbH is an innovative start-up from Gräfelfing near Munich and part of the SECOR Group. The company specializes in the development and provision of standardized semiconductors and software solutions for the automotive industry. With its revolutionary SDV CONCEPT (Software Defined Vehicle), SECOR sets new standards in the industry and offers OEMs (Original Equipment Manufacturers) significant advantages in development, production and after-sales.

Vision and mission

SECOR pursues the goal of making the automotive industry more resilient and economical. Through the standardization and interchangeability of hardware and software, the company aims to shorten product development times and increase the robustness of the supply chain. SECOR's vision is to prepare the automotive industry for future crises and make the use of vehicles more sustainable.

The SECOR SDV CONCEPT

At the heart of SECOR's innovation is the SDV CONCEPT. This concept is based on a zonal approach in which a central computer in the vehicle communicates with controllers in different zones. This drastically reduces the number of Electronic Control Units (ECUs) required, resulting in a simplified E/E architecture. SECOR's standardized semiconductors are software configurable and universally applicable, offering OEMs significant cost savings and flexibility.

Advantages for the automotive industry

Cost savings and efficiency: By using standardized semiconductors and the ability to replace them shortly before SOP (start of production), OEMs can reduce their production costs and increase efficiency. Since error-free, modular software packages (analogous to the x86 PC, keyword "IBM-compatible") can be used, this significantly accelerates the development process.

Resilience and sustainability: SECOR's chips are pin-compatible and can be replaced with higher-performance chips in the future, which extends the service life of vehicles and increases sustainability.

Flexibility and future-proofing: Thanks to the SDV CONCEPT, vehicle manufacturers can add new functionalities at a later date and change existing ones more easily. This enables continuous further development of the software without having to adapt the hardware.

Innovative technologies

SECOR relies on state-of-the-art technologies such as RISC-V cores and Linux as the operating system. This enables high flexibility and adaptability of the semiconductors. The chips are also equipped with FPGAs (Field Programmable Gate Arrays), which support AI functions and enable future applications.

Corporate structure

The SECOR Group consists of two business units:

1. *SECOR Chips & Library GmbH*: this unit develops the chips and provides the software that optimally supports them. The chips are manufactured in parallel by contract manufacturers on several continents in order to minimize geopolitical risks and increase the resilience of the supply chain.
2. *SECOR Supply Chain Transparency GmbH*: This unit offers SaaS tools (Software as a Service) that customers can use to identify and reduce supply chain risks. This enables better preparation for potential bottlenecks and a standardized resilience score assessment (parts tendering with R-factor).

Future prospects

SECOR plans to present the roadworthy SECOR SDV CONCEPT CAR for the first time at the IAA 2025. The company is already working with a development partner to achieve this goal. The presentation of the SECOR SDV CONCEPT at embedded world 2025 marks an important milestone in the company's history.

SECOR Chips & Library GmbH is a pioneer in the automotive industry and, with its SECOR SDV CONCEPT, offers a forward-looking solution for the challenges of the industry. Through the standardization of hardware and software, the flexibility and resilience of the supply chain, SECOR is setting new standards and preparing the automotive industry for a sustainable and efficient future.